Solaris Application Programming



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Reviews

The book is great and fantastic. It is writter in straightforward words and phrases rather than difficult to understand. You wont really feel monotony at at any time of your respective time (that's what catalogues are for regarding should you question me). **(Payton Miller)**

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paperback. Condition: New. Ship out in 2 business day, And Fast shipping, Free Tracking number will be provided after the shipment.Pages Number: 319 Publisher: Machinery Industry Press. Pub. Date :2008-5-1. This book describes how to get the Solaris operating system to run the program the best performance. The book is divided into five parts 13 chapters. including conventional processor. SPARC. x64 processor. information tools. compilers. floating point optimization. database and link performance analysis tools. calibration and debugging. performance counters measure. source code optimization. multi-core multi-threaded and multi-process performance analysis. Practical is the most important feature of the book. the book covers all of the current can be used on Solaris development tools. and describes their use. but the book also provides a number of instances of tool use and interpret the meaning of the output data. This book is rich in content. structured. not only for software development novice reading. are also available for experienced developers reference. Contents: Preface Part processor sequence translator Review Chapter 1 Chapter Objectives conventional processor 1.1 1.3 1.2 Composition of the processor clock rate of 1.4-order execution processor. chip multithreading 1.6 1.5 1.6.1 instruction execution pipeline latency 1.6. 2 load / store pipeline 1.6.3 1.6.4 branch pipes integer operation pipeline floating-point pipeline 1.6.5 1.8 1.7 cache bandwidth and latency system interaction 1.8.1 1.8.2 1.9.1 virtual memory system bus 1.9 Overview 1.9 .2 TLB and the page size and labeling index 1.10 1.11 memory instruction set architecture. SPARC family of Chapter 2 2.1 Chapter Objectives 2.2 UltraSPARC family 2.2.1 SPARC architecture history 2.2.2 UltraSPARC processor 2.3 SPARC instruction set 2.3.1 SPARC instruction 2.3.3 Introduction 2.3.2 integer register set register windows 2.3.4 2.4 32-bit floating-point registers and 64-bit code 2.5 UltraSPARC series processor core 2.5.2 2.5.1 CPU and memory prefetching 2

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