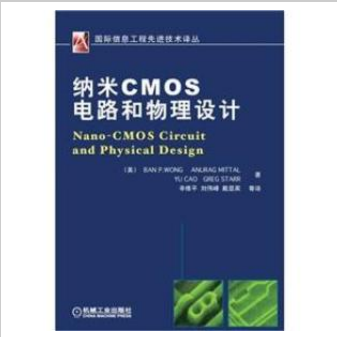

nano-CMOS circuit and physical design

By JIA XIN ZHANG

paperback. Condition: New. Ship out in 2 business day, And Fast shipping, Free Tracking number will be provided after the shipment.Paperback. Language: Chinese. Publisher: China Machine Press Pub. Date :2011-04-01. Nano-CMOS Circuit and Physical Design will nanometer process. device manufacturability. advanced circuit design and physical implementation and other content related to integrated together to form a set of advanced semiconductor technology. device and process of the new development. providing design considerations. focusing on the interaction between technology and design. and describes the design for manufacturing and the impact of volatility. Important topics include nano-CMOS technology designed to narrow the issues and their impact; subwavelength lithography; operational problems and solutions and theoretical physics; design for manufacturing and volatility. Nano-CMOS Circuit and Physical Design for IC designers and professionals in the field to read. Contents: Translator. then the original sequence in the original Preface Chapter 1 nanometer CMOS 1.1 narrowing the issues and implications of the design method of nano-CMOS 1.2 times performance improvement makes the continuity of innovation necessary to reduce the 1.3 sub 100nm lithography challenges and subwavelength 1.3.1 Review process. after the challenge (metal) are process challenges before 1.3.2 (transistor) 1.4 1.5 lithography process control and reliability issues and...

Reviews

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