



## CPLDFPGA [Paperback]

By BEN SHE.YI MING

paperback. Book Condition: New. Ship out in 2 business day, And Fast shipping, Free Tracking number will be provided after the shipment. Paperback Pages Number: 164 Language: Simplified Chinese. Publisher: Electronic Industry Press; 1st edition (November 1, 2011). CPLDFPGA technology of teaching, learning, practicing integrated teaching model to improve the practical engineering proficiency for the purpose of knowledge of the basic knowledge of the EDA technology and VHDL hardware description language, programmable logic devices, and development of software applications to run through a number of actual cases, learning can be a preliminary understanding and mastery of the reader by the CPLDFPGA technology The basic content of the EDA and practical techniques. The book is divided into six learning projects. Learning project through the design of the decoder, a brief introduction the basic knowledge of the EDA technology, the schematic entry method and circuit design process; learning Item 2 of the frequency meter design, the programmable logic device (CPLD and FPGA) chip structure, working principle and hierarchical schematic circuit diagram input method; learning project through the design and application of data selection, VHDL hardware description language program, the basic structure of the text method of circuit design software processes; learning projects 4...



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