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LOOP TILING FOR PARALLELISM

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FOR PARALLELISM

Jingling Xue

Springer-Verlag New York Inc., United States, 2012. Paperback. Book Condition: New. 235 x 155 mm. Language: English . Brand New Book. Loop tiling, as one of the most important compiler optimizations, is beneficial for both parallel machines and uniprocessors with a memory hierarchy. This book explores the use of loop tiling for reducing communication cost and improving parallelism for distributed memory machines. The author provides mathematical foundations, investigates loop permutability in the framework of nonsingular loop transformations, discusses the necessary...

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- Authored by Jingling Xue
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