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paperback. Condition: New. Ship out in 2 business day, And Fast shipping, Free Tracking number will be provided after the shipment.Pages Number: 427 Publisher: Harbin Institute of Technology Pub. Date :2007-08-01 version 3 this book from the application point of view. detailed description of the MCS-51 microcontroller hardware architecture. instruction set. a variety of hardware interface design. a variety of commonly used data processing operations and the design and interface drivers and MCS-51 microcontroller application design. and MCS-51 microcontroller applications design of new anti-jamming technology and devices are introduced in detail . This book highlights the usefulness of the content selected. typical. The book's application examples. mostly from research and teaching practices. and proven. content-rich. informative. This book can be used as engineering colleges of undergraduate. graduate. junior college students to learn the MCS-51 curriculum materials. also available in automatic control. intelligent instrumentation. testing. mechanical and electrical integration. and a variety of applications in the MCS-51 microcontroller engineering and technical officers. Contents: Chapter I Overview of single-chip micro-meter and other local history and development of SCM 1.1 Overview 1.2 single-chip microcontroller application development trends 1.3 1.3.1 1.3.2 single-chip microcontroller features the application of 1.4 8-bit microcontroller manufacturers and machine main Type 1.5 MCS-51 series of microcontrollers Chapter MCS-51 microcontroller hardware architecture 2.1 MCS-51 microcontroller hardware architecture 2.2 MCS-51 pin power supply and clock pin 2.2.1 2.2.2 2.2.3 IO control pin Port pin 2.3 MCS-51 microcontroller's central processor (CPU) 2.3.1 2.3.2 control computing unit components 2.4 MCS-51 program memory storage structure 2.4.1 2.4.3 2.4.2 internal data memory special function registers (SFR) 2.4.4-bit external data memory address space 2.4.5 2.5.1 IO 2.5 IO port port internal structure 2.5.2 I / O port read 2.5.3 I / O port write operation and reset the load capacity of 2.6 2.6.1 The register circuit...

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