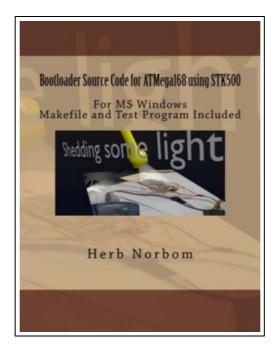
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Reviews

The publication is fantastic and great. It can be rally exciting throgh reading period of time. I am just very happy to inform you that this is the greatest publication i actually have read in my very own daily life and could be he very best ebook for at any time. (Prof. Alvis Wuckert)

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Createspace, United States, 2013. Paperback. Book Condition: New. 279 x 216 mm. Language: English . Brand New Book ***** Print on Demand ******. Step by Step instructions on how to put a bootloader on to the ATMega168 using the ISP STK500 programer. The how and why of the bootloader revealed, build your own bootloader. The complete source code is included. Using avr-gcc C programing language. Detailed instructions for hooking the STK500 to your computer and breadboard. Diagrams and instructions on building your breadboard included. This book starts with the assumptions that you want to know how to write a bootloader in the C programming language. That you want to learn how to use an ISP STK500 programer. You want to understand the microchip s fuses and lock bit settings and change them as you desire. That you want to load your own bootloader on to the microchip. You will cover the Makefile , for compiling your program and uploading on to your microchip. Learn how to build your own library for programs and headers that you want to include in your programs. This includes a uart.c program and a uart.h file. The steps needed to accomplish the loading of your bootloader are walked through giving the reader good direction. The exhibits that are included greatly enhance the visualization of the process. The book includes the complete source code for all programs and header files. The complete Makefiles are also provided. The source code and instructions for loading a test programs are also included. Even the eeprom memory is lightly covered. While this is a technical subject the author provides a great deal of insight and documentation on the process. The book goes into good depth without getting hopelessly lost in computer science lingo.



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