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TWO-TONE PLL: TWO-TONE PLL FOR ON-CHIP RF TEST



VDM Verlag Aug 2009, 2009. Taschenbuch. Condition: Neu. This item is printed on demand - Print on Demand Neuware - In present era VLSI ICs, production test is becomming increasingly expensive. Especially in case of mixed signal/RF chips, it can even override the direct manufacturing expenses. Under this context built inself test(BiST) can be viable solution to overcome this problem, which so far not appreciated by chip industry. In this book the two tone PLL provides solution and this circuit is intende for...

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- Authored by Muhammad Shuaib
- Released at 2009



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