



## Transaction Level Modeling and High Performance Simulation of Embedded Systems

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Shaker Verlag Okt 2013, 2013. Buch. Condition: Neu. Neuware - Transaction level modeling (TLM) is a simulation centric, system level modeling paradigm for the design of complex embedded systems incorporating both hardware and software. Compared to register transfer level (RTL) and signal level models, transaction level models require far less modeling effort and their simulation is orders of magnitude faster. Various TLM abstraction levels and modeling styles enable trade off between the simulation speed and accuracy for different stages of the system design flow. These have made TLM an increasingly valuable tool in the design of embedded systems, and an active research area in the recent years. TLM has evolved without an abstract formal foundation, around languages such as SystemC, which have inherited most of their modeling constructs from lower abstraction levels such as the gate level and the RTL. The main focus of TLM has been on constructs for abstract modeling of communication, and the description of behavior has relied on existing, low level modeling constructs. Moreover, although the trend is changing, current TLM languages are bound to sequential simulation schemes. These simulation schemes neither take advantage of the increasingly available low cost parallel processing power, nor from the...

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