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# TESTING METHODS FOR FAULT DETECTION IN ELECTRONIC CIRCUITS



Testing Methods For Fault  
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LAP Lambert Academic Publishing Dez 2014, 2014. Taschenbuch. Book Condition: Neu. 220x150x11 mm. Neuware - This book includes two testing methodologies based on Built In Sensors (BIS) and an optimization-based technique. The first part proposes two novel built-in sensors (BISs) for digital CMOS and analog circuits testing. The BISs have no voltage degradation, able to detect, identify and localize open and short circuit faults, have simple realizations with very small area and detection time. BIS is used to test a 4x4...

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- Authored by Rania F. Ahmed
- Released at 2014

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