Anatomy of a Silicon Compiler

By Brodersen, Robert W.

Book Condition: New. Publisher/Verlag: Springer, Berlin | A silicon compiler is a software system which can automatically generate an integrated circuit from a user's specification. Anatomy of a Silicon Compiler examines one such compiler in detail, covering the basic framework and design entry, the actual algorithms and libraries which are used, the approach to verification and testing, behavioral synthesis tools and several applications which demonstrate the system's capabilities. | 1. Introduction and History; R.W. Brodersen. Part I: Framework and Design Entry. 2. The OCT Data Manager; R. Spickelmier, B.C. Richards. 3. Lager OCT Policy and the SDL Language; B.C. Richards. 4. Schematic Entry; B. Reese. 5. Design Management; B.C. Richards. 6. Design Post-Processing; M. Thaler, B.C. Richards. Part II: Silicon Assembly. 7. Hierarchical Tiling; J. Sun, B.C. Richards. 8. Standard Cell Design; B. Reese, B. Boes. 9. Interactive Floorplanning; Seungjun Lee, J. Rabaey. 10. Datapath Generation; M. Srivastava. 11. Pad Routing; E. Lettang. Part III: Verification and Testing. 12. Design Verification; Wun-Tsin Jao, R. Jain. 13. Behavior and Switch Level Simulation; L. Svensson, L.E. Thon, Seungjun Lee. 14. Chip and Board Testing; K.T. Kornegay. Part IV: Behavioral Synthesis. 15. DSP Specification Using the Silage Language; P. Hilfinger, J. Rabaey. 16....

READ ONLINE
[1.57 MB]

Reviews

Extensive guide for ebook lovers. It generally does not cost excessive. Your way of life span will likely be convert the instant you complete looking at this ebook.
-- Rocky Dach

Certainly, this is the very best work by any author. It is amongst the most remarkable publication i have got study. I am just happy to inform you that this is actually the greatest pdf i have got study inside my individual daily life and can be he very best publication for at any time.
-- Gilbert Rippin